**Eric:**

Add actual line numbers to error messages

Output Data segment and both segments

Actual file output to another program

Finish instructions

Add detection for two labels, throw an error. Create test cases to test this

**Colby:**

Implemented line wrappers to visualize the wires on the CPU

Separate classes for Display, constants, and polygons

DMEM needs a dot for a branch point

Muxes need to be larger

Slanted lines

Move everything to the right to have more space

Clickable elements to display a new screen (Wiring diagram)

**Brady:**

Current version on visual CPU is on the server

Data needs 16 lines so if placing scrollable objects on the screen is not possible objects need to be larger

Print 16 in the data memory and 32 lines in the code memory. Both need a 6-bit address for the code and 4-bit memory for the data memory

Needs colors animating something over time to see the speed

**Jacob:**

Arrow keys working to switch between switches

Fix the input boxes updating the 0 and 1’s box

Fix problem of boxes disappearing

Early integration test with the main diagram

Seven segment indicator- 4-bit binary number and then display it

**Bryce:**

Working on the ALU (Overflow and carry flags)

Working on the clock for single cycle and pipeline

**Aiman:**

M.I.A